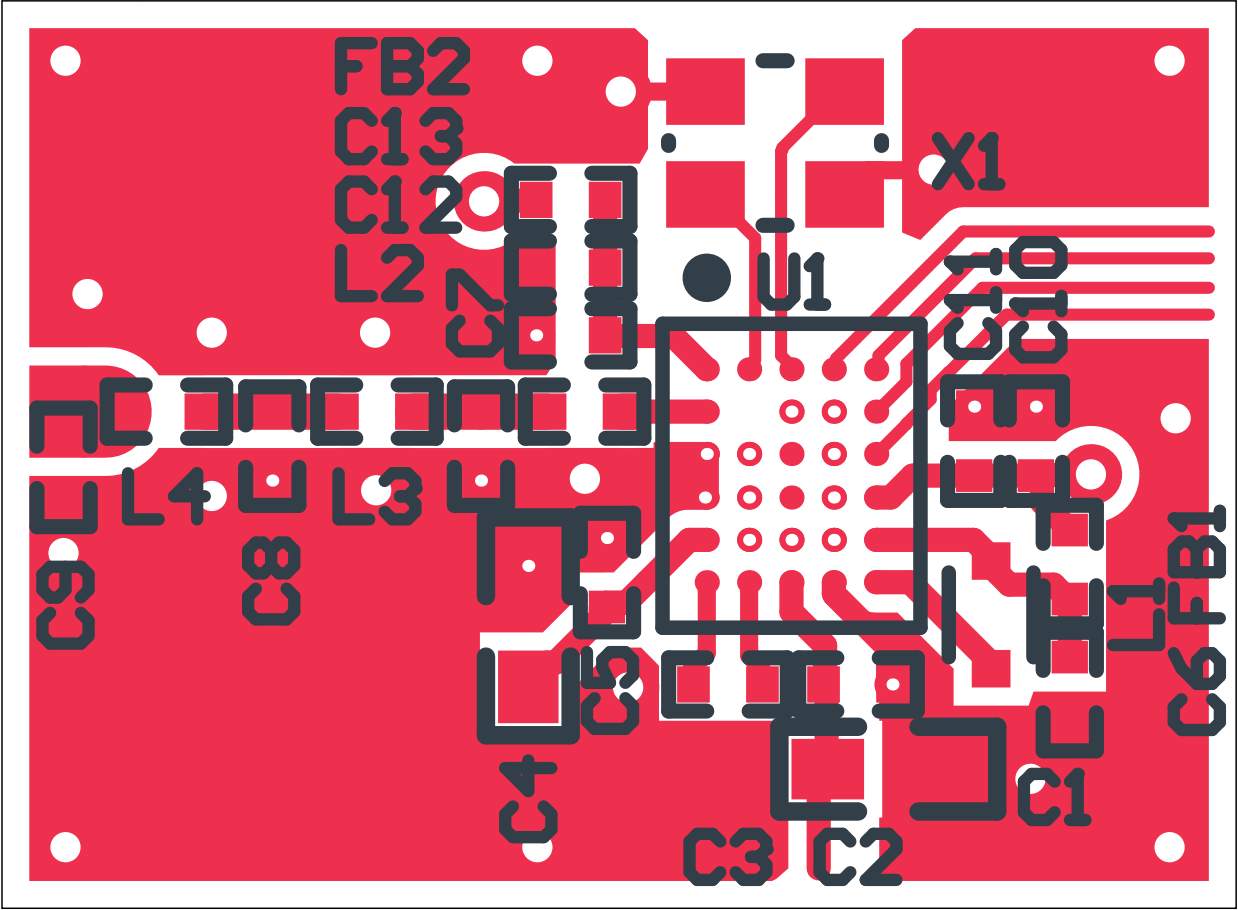


Top Layer (Scale 16:1)



Two out of four signals (P1.01, P1.02, P1.07 and P1.11) can be routed out on 4-layer PCB.

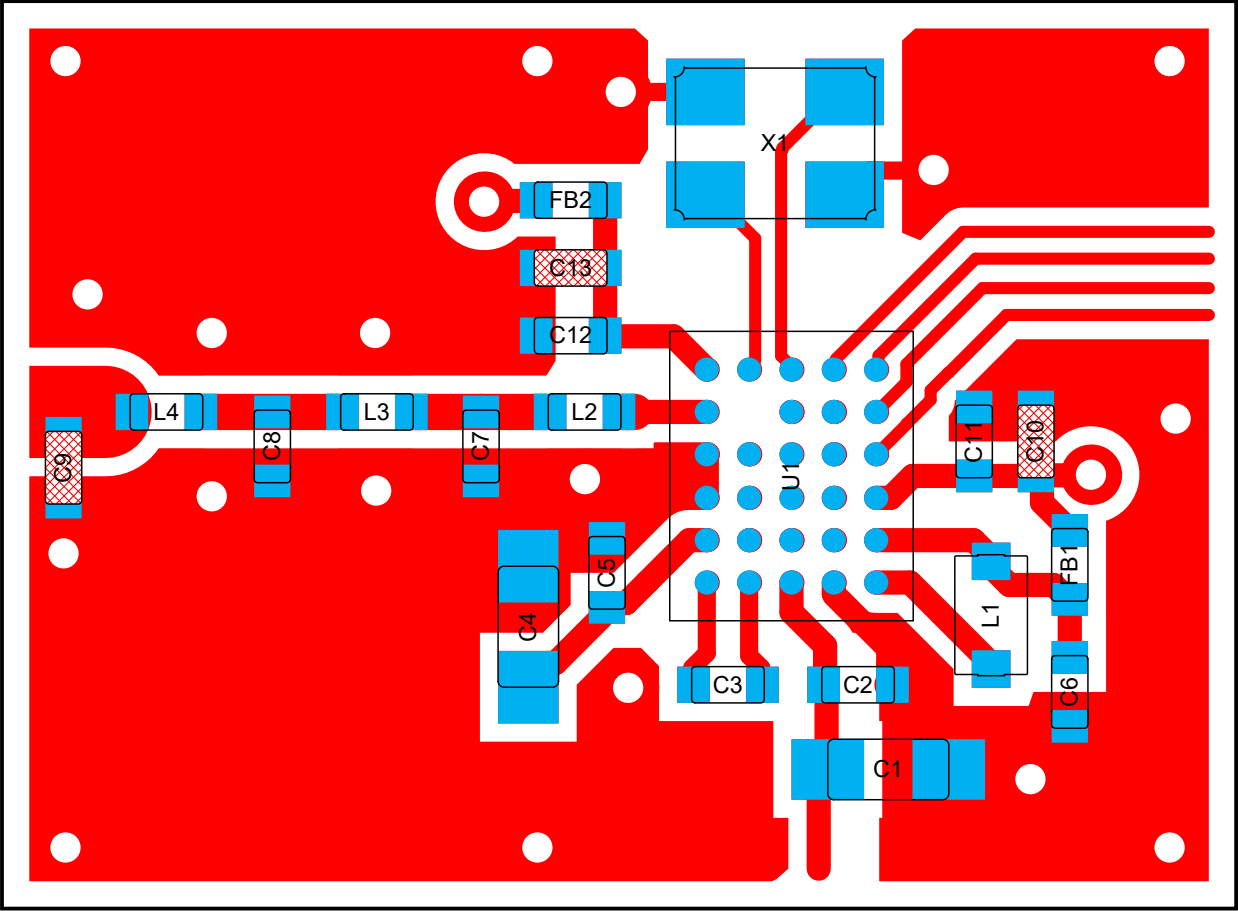
Due to on-chip magnetic interference, there must be no copper on the PCB top layer
between pins B1, C1 and A1.

Nordic Semiconductor does not warrant the accuracy or completeness of this documentation or any included information. Nordic Semiconductor makes no assurances that this design will meet specification, be appropriate for your application, or fit for any particular purpose, or that it will function in an implementation. Nordic Semiconductor also does not claim that the design is ready for production. It is your responsibility to thoroughly validate and test your design implementation to confirm the functionality of your application.
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Title VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 2	Device: nRF54LV10A-CAAA	Revision: 0.6
Date: 20.11.2025			Sheet 1 of 9
File: nRF54LV10A_caaa_config2.PCBDwf			Drawn By: DABA
Classification: Public			



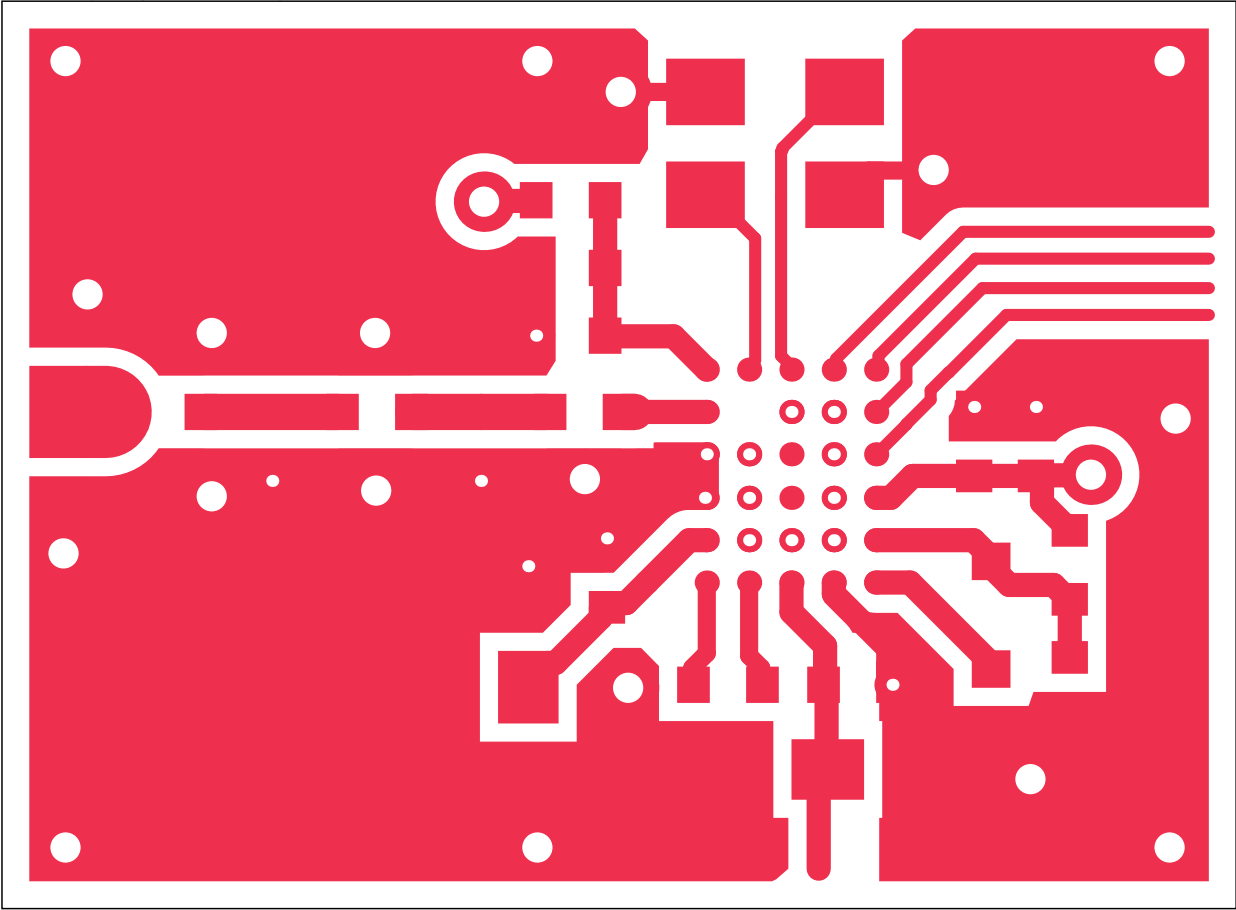
View from Top side (Scale 16:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 2	Device: nRF54LV10A-CAAA	Revision: 0.6
Date: 20.11.2025			Sheet 2 of 9
File: nRF54LV10A_aaaa_config2.PCBdWf			Drawn By: DABA
Classification: Public			



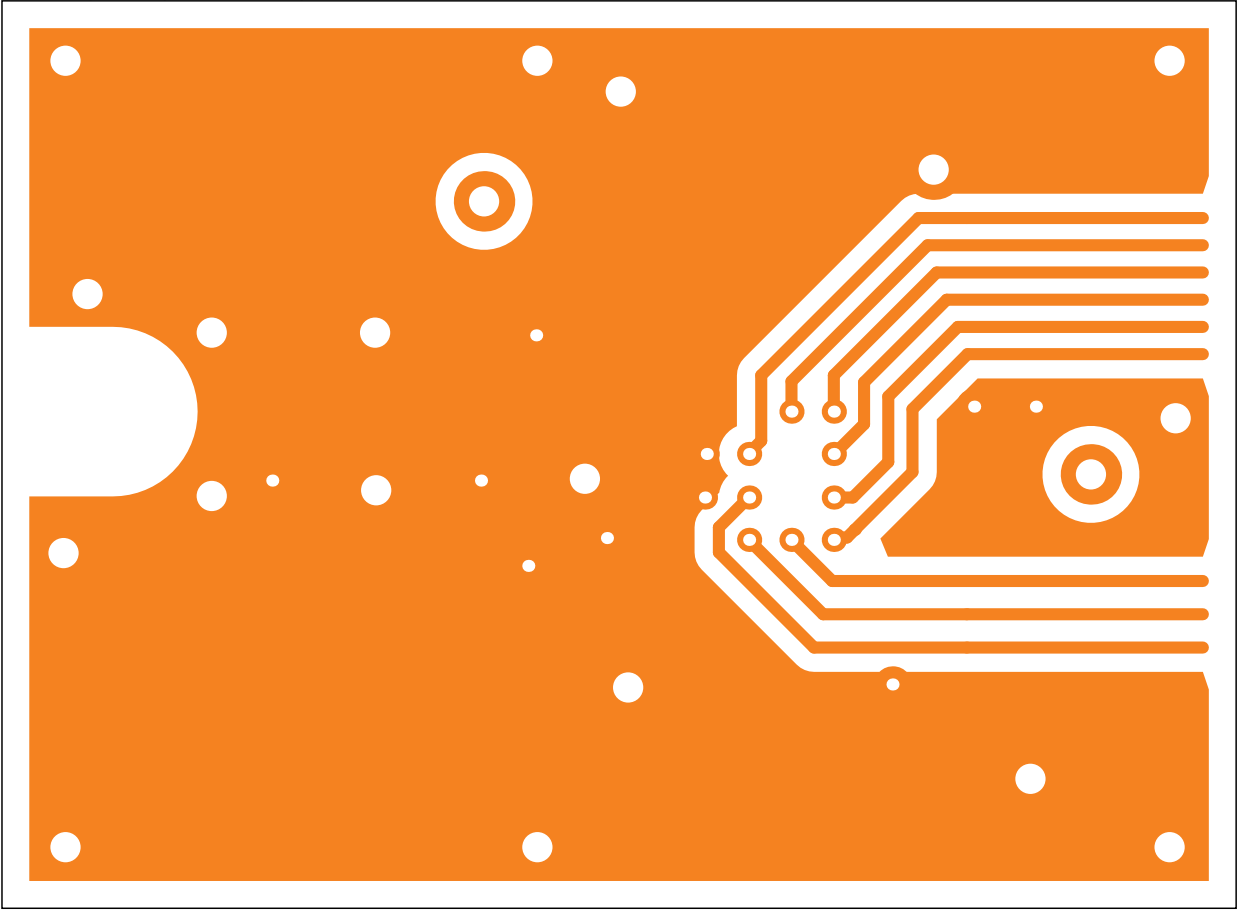
Top Layer (Scale 16:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	2
Date:	20.11.2025	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A_caaa_config2.PCBDwf	Revision:	0.6
Classification:	Public	Sheet	3 of 9
		Drawn By:	DABA



Mid-Layer 1 (Scale 16:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	2
Date:	20.11.2025	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A_caaa_config2.PCBDwf	Revision:	0.6
Classification:	Public	Sheet:	4 of 9
		Drawn By:	DABA



A

B

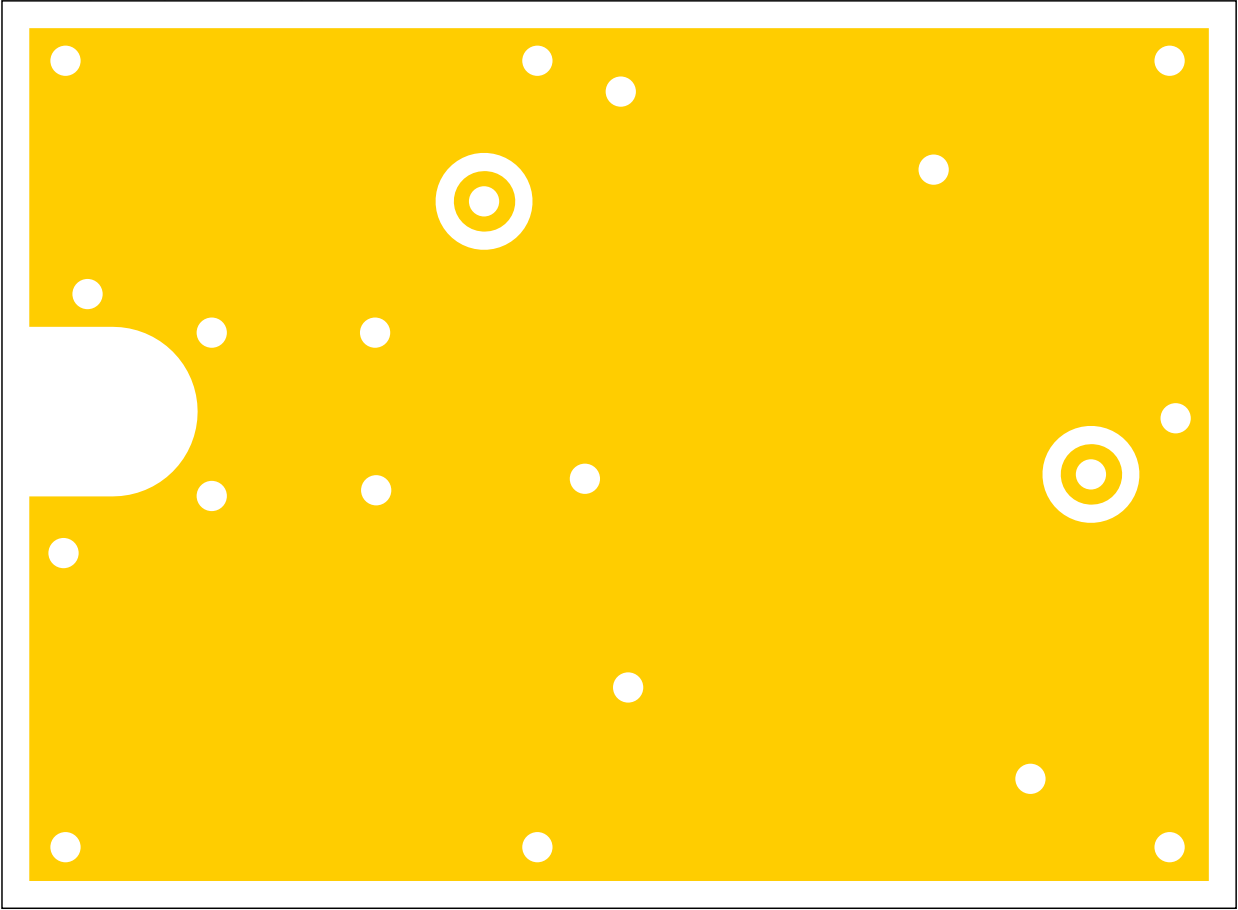
C

D

E

F

Mid-Layer 4 (Scale 16:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	2
Date:	20.11.2025	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A_caaa_config2.PCBDwf	Revision:	0.6
Classification:	Public	Sheet:	5 of 9
		Drawn By:	DABA



A

B

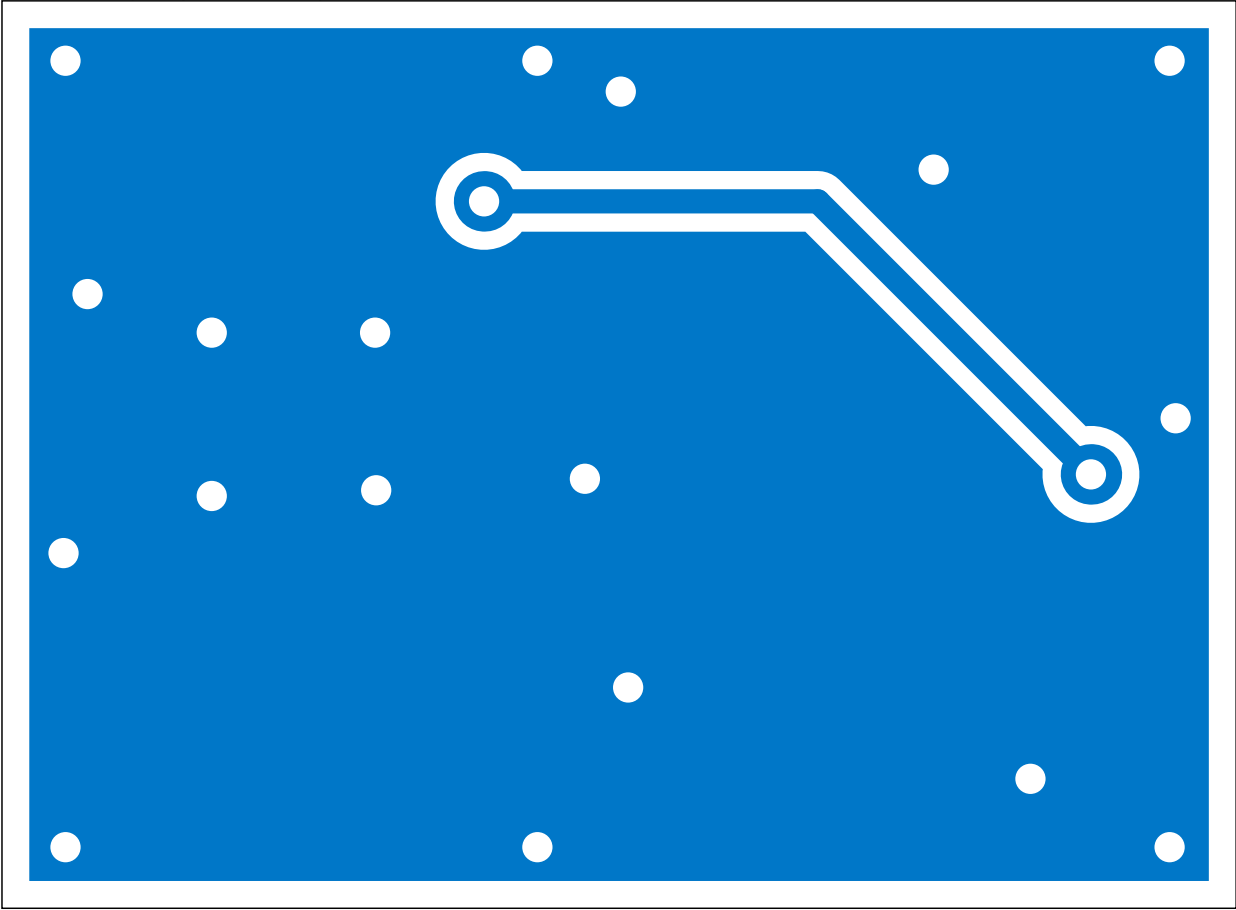
C

D

E

F

Bottom Layer (Scale 16:1)



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	2
Date:	20.11.2025	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A_caaa_config2.PCBDwf	Revision:	0.6
Classification:	Public	Sheet:	6 of 9
		Drawn By:	DABA



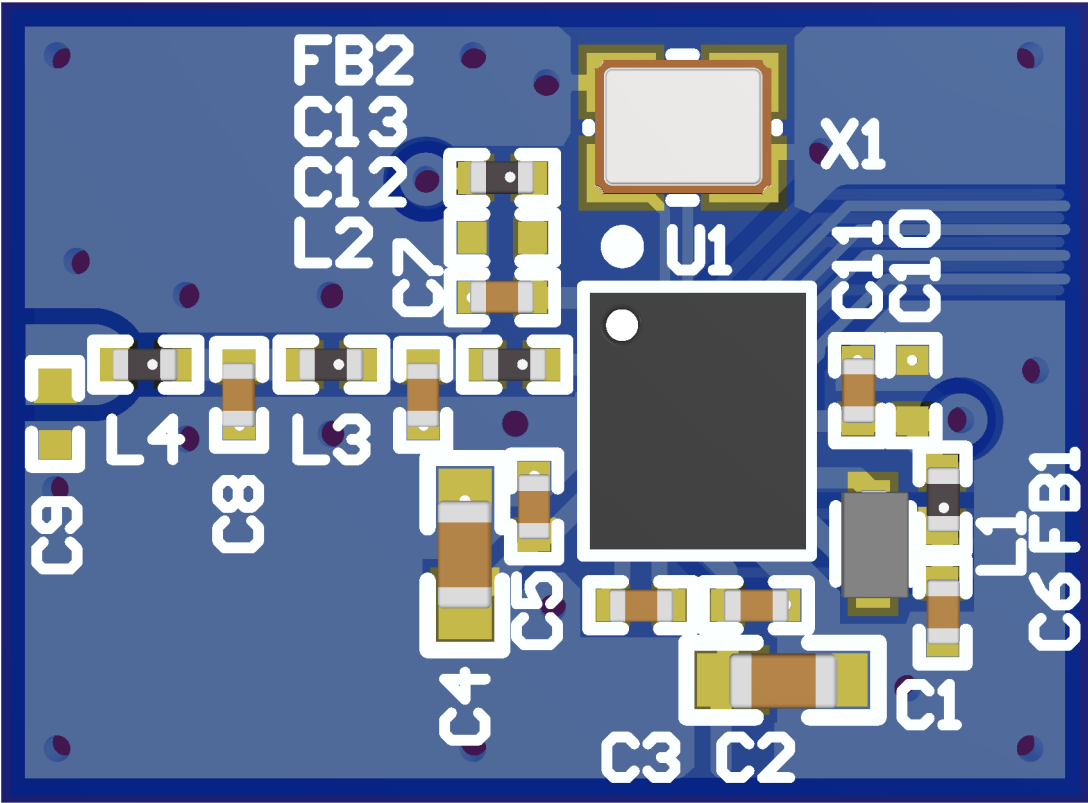
Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.03mm	SM-003	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
Prepreg		0.07mm		Dielectric	
Copper	Mid-Layer 1	0.04mm		Signal	G1
Core		1.30mm		Dielectric	
Copper	Mid-Layer 4	0.04mm		Signal	G2
Prepreg		0.07mm		Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Board Layer Stack Bottom Solder	0.03mm	SM-003	Solder Mask	GBS
	Board Layer Stack Bottom Overlay			Legend	GBO
Total thickness: 1.63mm					

Title VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size: A4	Config. No.: 2	Device: nRF54LV10A-CAAA	Revision: 0.6
Date: 20.11.2025			Sheet 7 of 9
File: nRF54LV10A_caaa_config2.PCBDwf			Drawn By: DABA
Classification: Public			



Realistic View



Title			
VDDL:nRF supplied by 1.5 V battery, Enabled DC/DC, 4-Layer PCB			
Size:	A4	Config. No.:	2
Date:	20.11.2025	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A_aaaa_config2.PCBDwf	Revision:	0.6
Classification:	Public	Sheet	8 of 9
		Drawn By:	DABA



